

## **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	) Examiner: Joseph H. Nguyen
Ali Keshavarzi et al.	) Art Unit: 2815
Application No.: 10/781,241  Filed: February 17, 2004	<ul> <li>I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:</li> </ul>
For: INSULATED CHANNEL FIELD EFFECT TRANSISTOR WITH AN ELECTRIC FIELD TERMINAL REGION	)
Assigned: Intel Corporation	) 6ay 67 11-1-2005 Signature Date

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Sir:

This is in response to two Office actions dated August 5, 2005. Both of the Office actions refer to serial number 10/781,241, but one is clearly mistakenly applied to this application because it makes no sense in the context of the application. For example, it lists claims 1-18, when in fact the application has claims 1-18 and 20-24, with claims 25-26 being withdrawn. Further, it rejects the claims under double patenting with respect to Patent 6,734,662 which makes no sense for this above-identified application.

Accordingly, the following responds to the merits of only the relevant Office action.

Reconsideration of the application is requested. Please amend the application as follows.